IN THE CLAIMS:

Claims 1-22 (Cancelled).

CLAIM 23 (PREVIOUSLY PRESENTED). A module for interfacing between a telephony network and a computer data network comprising:

a first connection coupled to said telephony network and adapted to receive telephony signals therefrom and output telephony signals thereto;

a second connection coupled to said computer data network and adapted to receive data packets therefrom and output data packets thereto;

a digital signal processor coupled to receive signals from said first connection and output signals thereto, wherein said digital signal processor detects events in said signals received from said first connection, wherein said events comprise at least one of caller ID, DTMF, call progress, and other forms of telephony signaling, and further wherein said digital signal processor provides outputs indicative of said detected events;

a synchronous-to-asynchronous converter that receives signals from said first connection and provides an asynchronous output therefrom; and

a control processor that receives said outputs from said digital signal processor indicative of detected events, and wherein said control processor outputs control messages over said data network to a call manager program installed on said data network, wherein said control messages are indicative of said events detected by said digital signal processor, and further wherein said control processor is also coupled to said synchronous-to-asynchronous converter for outputting asynchronous media streams over said data network via said second connection.

CLAIM 24 (PREVIOUSLY PRESENTED). The invention according claim 23, wherein said digital signal processor generates and receives multiple data streams.

CLAIM 25 (PREVIOUSLY PRESENTED). The invention according to claim 23, wherein said network is an ATM network.

CLAIM 26 (PREVIOUSLY PRESENTED). The invention according to claim 23, wherein said network is an ethernet network.

CLAIM 27(PREVIOUSLY PRESENTED). The invention according to claim 26, wherein said network is a cells in frames ethernet network.

CLAIM 28(PREVIOUSLY PRESENTED). The invention according to claim 23, wherein said network is an internet protocol over ATM network.

CLAIM 29(PREVIOUSLY PRESENTED). The invention according to claim 23, wherein said network is an internet protocol over an ethernet network.

CLAIM 30(PREVIOUSLY PRESENTED). The invention according to claim 23, wherein said computer data network also carries computer data traffic.

CLAIM 31(CANCELLED).

CLAIM 32 (PREVIOUSLY PRESENTED). The invention according to claim 23, wherein said synchronous-to-asynchronous converter uses first-in-first-out buffering.

CLAIMS 33-39 (CANCELLED). Without prejudice.